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Patent Application for: Patel et al.

Applicants:

Atty. No: SIO-0102

Title: Low Loss SOI/CMOS Compatible Silicon
Waveguides and Method of Making
the Same

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Signature: Wendy W. Koba



IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

CASE *SIO-0102*

SERIAL NO. *10/806,738*

GROUP ART UNIT *1765*

FILED *March 23, 2004*

EXAMINER *Anita Karen Alanko*

TITLE *Low Loss SOI-CMOS Compatible Silicon Waveguides
and method of making the same*

COMMISSIONER OF PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

SIR

Enclosed is an amendment in the above-identified application:

☒ No additional fee is required, as shown below.

CLAIMS AS AMENDED

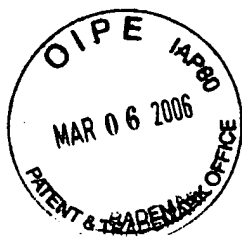
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL CLAIMS FOR FEE PURPOSES	<i>34</i>	MINUS	<i>42</i>	<i>0</i>	X \$50	<i>0</i>
INDEPENDENT CLAIMS	<i>3</i>	MINUS	<i>2</i>	<i>0</i>	X \$200	<i>0</i>
MULTIPLE DEPENDENT CLAIM(S) First presented with this amendment, if applicable					X\$360	<i>0</i>
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ <i>0</i>

Please address all correspondence to Wendy W. Koba, P.O. Box 556, Springtown, PA 18081-0556.

Respectfully,

Wendy W. Koba
Wendy W. Koba
Reg. No. 30509
Attorney for Applicant(s)
610-346-7112

Date: *3/6/2004*



SIO-0102

**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE**

Patent Application

Inventor(s) Vipulkumar Kantilal Patel et al.

Case SIO-0102

Conf. No. 2537

Serial No. 10/806,738

Group Art Unit 1765

Filing Date March 23, 2004

Examiner Anita Karen Alanko

Title Low Loss SOI/CMOS Compatible Silicon Waveguide and
Method of Making the Same

**COMMISSIONER OF PATENTS
WASHINGTON, D.C. 20231**

SIR:

AMENDMENT

In response to the Office action dated December 15, 2005, please amend the
above-identified application as follows: